



PATENT APPLICATION

B/B
1/30/03
Cmbs

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Docket No: Q56457

Luc DARTOIS

RECEIVED

JAN 29 2003

Appln. No.: 09/429,026

Group Art Unit: 2631

Technology Center 2600

Confirmation No.: 5442

Examiner: Qutbuddin GHULAMALI

Filed: October 29, 1999

For: METHOD FOR LINEARISING A POWER AMPLIFIER OVER A WIDE FREQUENCY BAND

AMENDMENT UNDER 37 C.F.R. § 1.111

Commissioner for Patents
Washington, D.C. 20231

Sir:

In response to the Office Action dated August 26, 2002, please amend the above-identified application as follows:

IN THE SPECIFICATION:

Page 6, the fifth full paragraph beginning "The signals to be", amend as follows:

The signals to be amplified by power amplifier 10 are samples at a sample frequency of 32,768 megahertz. These samples are, classically, represented by their complex coordinates I and Q. The coordinates I and Q are applied to the respective 20 inputs 22₁ and 22₂ of a complex multiplier accumulator (CMAC) 22, the function of which will be described later. These two coordinates I and Q, which represent the real time signal in rectangular coordinates are converted into polar coordinates by a Cordic converter 24. This converter 24 has two outputs 24₁ and 24₂.

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